

Abstract of the Disclosure:

A circuit configuration is provided for level boosting, in particular for driving a link that can be programmed by an energy pulse, which is also referred to as a fuse. The

5 circuit configuration has a circuit for level boosting and also a logic circuit. The logic circuit combines a first input signal with a second input signal and controls an input of the circuit for level boosting, the output level of an output signal of the circuit for level boosting being greater

10 than the input level. A fusible link can be connected to an output terminal of the circuit for level boosting. Since an input stage of the circuit for level boosting is at the same time a first subcircuit of the logic circuit, the circuit configuration enables an exceptional component and area-saving

15 construction. This has an advantageous effect particularly in mass memory chips.

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